	Application No.	Applicant(s)	
		:	
Notice of Allowability	09/653,070 Examiner	MIZUNO, MASAYUKI Art Unit	
·			
	Harry Vartanian	2634	<del></del>
The MAILING DATE of this communication appears on the cover sheet with the correspondence address All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS. This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.			
1. This communication is responsive to 8/5/2004.			
2. The allowed claim(s) is/are 1,2,4-7 and 9-32.			
3. The drawings filed on 13 February 2004 are accepted by the Examiner.			
<ul> <li>4. Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).</li> <li>a) All b) Some* c) None of the: <ol> <li>Certified copies of the priority documents have been received.</li> <li>Certified copies of the priority documents have been received in Application No.</li> <li>Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).</li> </ol> </li> </ul>			
* Certified copies not received:		:	
Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.  THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.			
5. A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.			
6. CORRECTED DRAWINGS (as "replacement sheets") must be submitted.			
(a) ☐ including changes required by the Notice of Draftsperson's Patent Drawing Review ( PTO-948) attached			
1) 🗌 hereto or 2) 🔲 to Paper No./Mail Date			
(b) ☐ including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date			
Identifying indicia such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).			
7. DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.			
Attachment(s) 1. ☑ Notice of References Cited (PTO-892) 2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-948)	6. 🔲 Interview Summa		152)
3. Information Disclosure Statements (PTO-1449 or PTO/SB/0	Paper No./Mail D 8), 7. ⊠ Examiner's Amen	Date Idment/Comment	
Paper No./Mail Date  4.  Examiner's Comment Regarding Requirement for Deposit	8. 🛛 Examiner's Statement of Reasons for Allowance		
of Biological Material	9.  Other	:	
		:	

**EXAMINER'S AMENDMENT** 

1. An extension of time under 37 CFR 1.136(a) is required in order to make an

examiner's amendment which places this application in condition for allowance. During a

telephone conversation conducted on 9/3/2004, Frank L. Bernstein(Registration # 31484)

requested an extension of time for 1 MONTH(S) and authorized the Director to charge

Deposit Account No. 19-4880 the required fee of \$110 for this extension and authorized the

following examiner's amendment. Should the changes and/or additions be unacceptable to

applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure

consideration of such an amendment, it MUST be submitted no later than the payment of

the issue fee.

The application has been amended as follows:

Please change the following in Claim 1, line 2:

transmitting data signals[[;]], said interconnect circuit comprising:

Please change the following in Claim 1, lines 5-10:

[[wherein said data line selectively interrupts and reestablishes transmission of said data

signals at selected portions of said data line responsive to said congestion signals.]]

wherein said data line comprises a plurality of data driving circuits which selectively

interrupt and reestablish transmission of said data signals responsive to said congestion signals

and wherein said plurality of data driving circuits store said data signals temporarily when said

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transmission of said data signals is interrupted.

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Please change the following in Claim 6, lines 3-8:

Page 3

Application/Control Number: 09/653,070

Art Unit: 2634

[[wherein said additional data lines selectively interrupt and reestablish transmission of

said additional data signals at selected portions of said additional data lines responsive to said

congestion signals.]] wherein said one or more additional data lines comprise a plurality of data

driving circuits which selectively interrupt and reestablish transmission of said data signals

responsive to said congestion signals and wherein said plurality of data driving circuits store said

data signals temporarily when said transmission of said data signals is interrupted.

Please change the following in Claim 9, lines 1:

An interconnect circuit according to claim  $\underline{1}$  [[8]] wherein

Please change the following in Claim 10, lines 1:

An interconnect circuit according to claim  $\underline{1}$  [[8]] wherein

Please change the following in Claim 26, line 2

signals through an interconnect[[;]], said method comprising:

CANCEL CLAIMS 3 AND 8.

**Reasons for Allowance** 

2. The following is an examiner's statement of reasons for allowance: Claims 1, 15, and

26 have been amended to clearly show that the invention pertains to a system for

managing congestion in an integrated circuit, and not a data network like ATM. The prior

art for integrated circuits failed to clearly show such a congestion management system, in

particular one that:

Application/Control Number: 09/653,070

Art Unit: 2634

"selectively interrupt and reestablish transmission of said data signals responsive to

Page 4

said congestion signals"

Any comments considered necessary by applicant must be submitted no later than

the payment of the issue fee and, to avoid processing delays, should preferably accompany

the issue fee. Such submissions should be clearly labeled "Comments on Statement of

Reasons for Allowance."

Any inquiry concerning this communication or earlier communications from the

examiner should be directed to Harry Vartanian whose telephone number is 571.272.3048.

The examiner can normally be reached on 10:00-6:30 Mondays to Fridays.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's

supervisor, Stephen Chin can be reached on 571.272.3056. The fax phone number for the

organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent

Application Information Retrieval (PAIR) system. Status information for published

applications may be obtained from either Private PAIR or Public PAIR. Status information

for unpublished applications is available through Private PAIR only. For more information

about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on

access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-

217-9197 (toll-free).

Harry Vartanian Examiner

Art Unit 2634

HV

STEPHEN CHIN

SUPERVISORY PATENT EXAMINE

TECHNOLOGY CENTER 2600